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	L56	((bit or bits) near lock)	1726
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	L53	(two-bit lock)	738242
	L52	L49 and (lock near (bit or bits))	4
	L51	L49 and (lock with (bit or bits))	11
	L50	L49 and lock	63
	L49	L20 and address.ti.	1762
	L48	L47 and address.ti.	0
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	L46	L45 and (word or words)	1
· 🗖	L45	6987813.pn.	1
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	L43	L20 and (address same word same n same (bit or bits))	1252
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	L41	L40 and encod\$	51
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	L39	address.ti.	6442
	L38	(address same word same n same (bit or bits))	6799
Man	23	8	

L37	L36 and (n near (bit or bits))	0	
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L35	((object or objects) with virtual with encode with (bit or bits) with address)	0	
L34	L32 and memory	20	
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L32	L31 and (pointer or pointers)		
L31	L30 and encod\$		
L30	L28 and header.ab.		
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L26	L24 and L25		
L25	L20 and (encod\$ near lock\$)	78	
L24	L23 and (pointer or pointers)	2	
L23	L22 and lock	2	
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L21	L20 and (address near k-bit)	15	
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AbstractPlus | Full Text: PDF(1374 KB) | IEEE CNF Rights and Permissions 13. Virtual page tag reduction for low-power TLBs Petrov, P.; Orailoglu, A.; Computer Design, 2003. Proceedings, 21st International Conference on 13-15 Oct. 2003 Page(s):371 - 374 Digital Object Identifier 10.1109/ICCD.2003.1240921 AbstractPlus | Full Text: PDF(270 KB) IEEE CNF Rights and Permissions 14. Using a single address space operating system for distributed computing performance Skousen, A.; Miller, D.; Performance, Computing and Communications Conference, 1999. IPCCC '99. International 10-12 Feb. 1999 Page(s):8 - 14 Digital Object Identifier 10.1109/PCCC.1999.749414 AbstractPlus | Full Text: PDF(660 KB) IEEE CNF Rights and Permissions 15. Don't Use the Page Number, but a Pointer to It Seznec, S.; Computer Architecture, 1996 23rd Annual International Symposium on 22-24 May 1996 Page(s):104 - 104 Digital Object Identifier 10.1109/ISCA.1996.10025 AbstractPlus | Full Text: PDF(976 KB) IEEE CNF Rights and Permissions 16. Supporting Reference And Dirty Bits In SPUR's Virtual Address Cache \blacksquare Wood, D.A.; Katz, R.H.; Computer Architecture, 1989. The 16th Annual International Symposium on 28 May - 1 June, 1989 Page(s):122 - 130 AbstractPlus | Full Text: PDF(868 KB) | IEEE CNF Rights and Permissions 17. SPARC64: a 64-b 64-active-instruction out-of-order-execution MCM proce Williams, T.; Patkar, N.; Shen, G.; Solid-State Circuits, IEEE Journal of Volume 30, Issue 11, Nov. 1995 Page(s):1215 - 1226 Digital Object Identifier 10.1109/4.475709 AbstractPlus | Full Text: PDF(1656 KB) | IEEE JNL Rights and Permissions 18. A low-power 2.5-GHz 90-nm level 1 cache and memory management unit Haigh, J.R.; Wilkerson, M.W.; Miller, J.B.; Beatty, T.S.; Strazdus, S.J.; Clark, L Solid-State Circuits, IEEE Journal of Volume 40, Issue 5, May 2005 Page(s):1190 - 1199 Digital Object Identifier 10.1109/JSSC.2005.845971 AbstractPlus | References | Full Text: PDF(1160 KB) | IEEE JNL Rights and Permissions

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"Topologies"—distributed objects on multicomputers



Karsten Schwan, Win Bo

May 1990 ACM Transactions on Computer Systems (TOCS), Volume 8 Issue 2

Publisher: ACM Press

Full text available: pdf(3.83 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

Application programs written for large-scale multicomputers with interconnection structures known to the programmer (e.g., hypercubes or meshes) use complex communication structures for connecting the applications' parallel tasks. Such structures implement a wide variety of functions, including the exchange of data or control information relevant to the task computations and/or the communications required for task synchronization, message forwarding/filtering under program control, and so o ...

² A comparison of the object-oriented features of Ada 95 and Java



Benjamin M. Brosgol

November 1997 Proceedings of the conference on TRI-Ada '97

Publisher: ACM Press

Full text available: pdf(2.41 M3)

Additional Information: full citation, references, citings, index terms

A dynamic network architecture



Sean W. O'Malley, Larry L. Peterson

May 1992 ACM Transactions on Computer Systems (TOCS), Volume 10 Issue 2

Publisher: ACM Press



Additional Information: full citation, abstract, references, citings, index terms, review

Network software is a critical component of any distributed system. Because of its complexity, network software is commonly layered into a hierarchy of protocols, or more generally, into a protocol graph. Typical protocol graphs—including those standardized in the ISO and TCP/IP network architectures—share three important properties; the protocol graph is simple, the nodes of the graph (protocols) encapsulate complex functionality, and the topology of the graph ...

Keywords: composibility, dynamic configuration, reuse



4 A formal description of the UNIX operating system

Thomas W. Doeppner, Alessandro Glacalone

August 1983 Proceedings of the second annual ACM symposium on Principles of distributed computing PODC '83

Publisher: ACM Press

Full text available: pdf(916.14 KB)

Additional Information: full citation, abstract, references, citings, index terms

In this paper we discuss our approach to a formal description of the UNIX operating system [Rit78a] [Rit78b] [Tho78], using Milner's Calculus of Communicating Systems (CCS) [Mil80]. The paper focuses on the problems one encounters and the decisions one has to make when describing a system such as UNIX. We believe that the problems that arise in defining such a system are much less well understood than those, for example, related to the formalization of programmin ...

5 A framework for the assessment of operating systems for small computers

🛦 Hossein Saiedian, Munib Siddiqi

April 1996 ACM SIGICE Bulletin, Volume 21 Issue 4

Publisher: ACM Press

Full text available: pdf(1.69 MG) Additional Information: full citation, abstract, references, index terms

A number of high performance operating systems are now available for small computers on different hardware platforms. These operating systems offer many advanced features formerly reserved for their workstation and minicomputer counterparts. This article surveys the most widely used of such operating systems, namely OS/2, Windows NT, Linux and Macintosh System 7.5. It provides an account on the history, design objectives and evolution of these operating systems and discusses their key features, ...

Keywords: CP/M, DOS, Linux, Macintosh, Microcomputers, OS/2, Operating Systems, Small Computer Systems, Windows, Windows NT

A high performance, universal, key associative access method

David B. Lomet

May 1983 ACM SIGMOD Record, Proceedings of the 1983 ACM SIGMOD international conference on Management of data SIGMOD '83, Volume 13 Issue 4

Publisher: ACM Press

Full text available: pdf(1.81 MS) Additional Information: full citation, abstract, references, citings

A new file organization is proposed that combines the advantages of digital B-trees and extendible hashing methods into one organization that can be used universally. The method, like these predecessors, relies on digital searching. The key notions are: (i) that multipage nodes are addressed by the root and can have both data and index entries, the mix of entries changing over time; and (ii) that these nodes can be doubled with file growth and, when this occurs, data nodes at the next level of t ...

A high-level abstraction of shared accesses

Peter J. Keleher

February 2000 ACM Transactions on Computer Systems (TOCS), Volume 18 Issue 1

Publisher: ACM Press

Full text available: pdf(183.57 KB) Additional Information: full citation, abstract, references, index terms,

We describe the design and use of the tape mechanism, a new high-level abstraction of accesses to shared data for software DSMs. Tapes consolidate and generalize a number of





recent protocol optimizations, including update-based locks and recorded-replay barriers. Tapes are usually created by "recording" shared accesses. The resulting recordings can be used to anticipate future accesses by tailoring data movement to application semantics. Tapes-based mechanisms a ...

Keywords: DSM, programming libraries, shared memory, update protocols

8 A history and evaluation of System R

Donald D. Chamberlin, Morton M. Astrahan, Michael W. Blasgen, James N. Gray, W. Frank King, Bruce G. Lindsay, Raymond Lorie, James W. Mehl, Thomas G. Price, Franco Putzolu, Patricia Griffiths Selinger, Mario Schkolnick, Donald R. Slutz, Irving L. Traiger, Bradford W. Wade, Robert A. Yost

October 1981 Communications of the ACM, Volume 24 Issue 10

Publisher: ACM Press

Full text available: pdf(1.55 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

System R, an experimental database system, was constructed to demonstrate that the usability advantages of the relational data model can be realized in a system with the complete function and high performance required for everyday production use. This paper describes the three principal phases of the System R project and discusses some of the lessons learned from System R about the design of relational systems and database systems in general.

Keywords: access path selection, authorization, compilation, database management systems, locking, recovery, relational model

9 A Model-Based Approach for Executable Specifications on Reconfigurable Hardware Tim Schattkowsky, Wolfgang Mueller, Achim Rettberg



March 2005 Proceedings of the conference on Design, Automation and Test in Europe - Volume 2 DATE '05

Publisher: IEEE Computer Society

Full text available: pdf(174.47 KB) Additional Information: full citation, abstract, index terms

UML 2.0 provides a rich set of diagrams for systems documentation and specification. Many efforts have been undertaken to employ different aspects of UML for multiple domains, mainly in the area of software systems. Considering the area of electronic design automation, however, we currently see only very few approaches, which investigate UML for hardware design and hardware/software co-design. In this article, we present an approach for executable UML closing the gap from system specification to ...

10 A new page table for 64-bit address spaces



December 1995 ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles SOSP '95, Volume 29

Publisher: ACM Press

Full text available: pdf(1.97 MB)

Additional Information: full citation, references, citings, index terms

11 A new switch chip for IBM RS/6000 SP systems

Craig B. Stunkel, Jay Herring, Bulent Abali, Rajeev Sivaram
January 1999 Proceedings of the 1999 ACM/IEEE conference

January 1999 Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM)

Publisher: ACM Press

Full text available: pdf(177.66 KB) Additional Information: full citation, references, citings, index terms

12 A personal view of the personal work station: some firsts in the Fifties

Douglas Ross

January 1986 Proceedings of the ACM Conference on The history of personal workstations

Publisher: ACM Press

Full text available: pdf(4.26 MB)

Additional Information: <u>full citation</u>, <u>references</u>, <u>index terms</u>

13 A portable sampling-based profiler for Java virtual machines



John Whaley

June 2000 Proceedings of the ACM 2000 conference on Java Grande

Publisher: ACM Press

Full text available: pdf(1.01 M8) Additional Information: full citation, references, citings, index terms

14 A Robust Main-Memory Compression Scheme



May 2005 ACM SIGARCH Computer Architecture News, Proceedings of the 32nd Annual International Symposium on Computer Architecture ISCA '05,

Volume 33 Issue 2 Publisher: IEEE Computer Society, ACM Press

Full text available: pdf(460,04 KB) Additional Information: full citation, abstract, index terms

Lossless data compression techniques can potentially free up more than 50% of the memory resources. However, previously proposed schemes suffer from high access costs. The proposed main-memory compression scheme practically eliminates performance losses of previous schemes by exploiting a simple and yet effective compression scheme, a highly-efficient structure for locating a compressed block in memory, and a hierarchical memory layout that allows compressibility of blocks to vary with a low fra ...

15 A single intermediate language that supports multiple implementations of exceptions





Norman Ramsey, Simon Peyton Jones

May 2000 ACM SIGPLAN Notices, Proceedings of the ACM SIGPLAN 2000 conference on Programming language design and implementation PLDI '00, Volume 35 Issue 5

Publisher: ACM Press

Full text available: pdf(900,75 KB)

Additional Information: full citation, abstract, references, citings, index

We present mechanisms that enable our compiler-target language, C--, to express four of the best known techniques for implementing exceptions, all within a single, uniform framework. We define the mechanisms precisely, using a formal operational semantics. We also show that exceptions need not require special treatment in the optimizer; by introducing extra dataflow edges, we make standard optimization techniques work even on programs that use exceptions. Our approach clarifies the design s ...

16 A survey of current object-oriented databases



Mansour Zand, Val Collins, Dale Caviness

February 1995 ACM SIGMIS Database, Volume 26 Issue 1

Publisher: ACM Press

Full text available: pdf(1.44 M3) Additional Information: full citation, abstract, citings, index terms

Object-oriented concepts form a good basis for the data models required for next-generation database applications such as CAD/CAE/CASE/CAM systems, knowledge-based systems, multimedia, etc. Many object-oriented databases are available commercially or are being developed by industry or academic research facilities. This paper attempts to compare some of these products using fourteen criteria. The selected criteria are major factors required for the successful design of an object-oriented database ...

Keywords: OOD-BMS survey, object-oriented database, object-oriented terminology

17 A taxonomy of computer program security flaws

Carl E. Landwehr, Alan R. Bull, John P. McDermott, William S. Choi September 1994 ACM Computing Surveys (CSUR), Volume 26 Issue 3

Publisher: ACM Press

Full text available: pdf(3.81 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

An organized record of actual flaws can be useful to computer system designers, programmers, analysts, administrators, and users. This survey provides a taxonomy for computer program security flaws, with an Appendix that documents 50 actual security flaws. These flaws have all been described previously in the open literature, but in widely separated places. For those new to the field of computer security, they provide a good introduction to the characteristics of security flaws and how they ...

Keywords: error/defect classification, security flaw, taxonomy

18 A Tree Structured Architecture for semantic gap reduction

Arieh Plotkin, Daniel Tabak

September 1983 ACM SIGARCH Computer Architecture News, Volume 11 Issue 4

Publisher: ACM Press

Full text available: pdf(938.46 KB) Additional Information: full citation, abstract, references

The article proposes a new Tree-Structured-Architecture (TSA). The TSA is object-oriented, implements the notions of capability-based-addressing and the single-level-store, and it is particularly designed to narrow the semantic gap. It encourages modular programming and directly supports the concepts of tasks and inter-task communication, making it particularly suitable for multiprocessing and multiprogramming implementation. The TSA is implemented on a multi-resource, distributed, matrix-structu ...

19 A unified vector/scalar floating-point architecture

N. P. Jouppi, J. Bertoni, D. W. Wall

April 1989 ACM SIGARCH Computer Architecture News, Proceedings of the third international conference on Architectural support for programming languages and operating systems ASPLOS-III, Volume 17 Issue 2

Publisher: ACM Press

Full text available: pdf(1.25 M3)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> <u>ierms</u>

In this paper we present a unified approach to vector and scalar computation, using a single register file for both scalar operands and vector elements. The goal of this architecture is to yield improved scalar performance while broadening the range of vectorizable applications. For example, reduction operations and recurrences can be expressed in vector form in this architecture. This approach results in greater overall performance for most applications than does the approach of emphasizin ...



20 Accent: A communication oriented network operating system kernel



Richard F. Rashid, George G. Robertson

December 1981 Proceedings of the eighth ACM symposium on Operating systems principles

Publisher: ACM Press

Full text available: pdf(1.01 M3)

Additional Information: full citation, abstract, references, citings, index

Accent is a communication oriented operating system kernel being built at Carnegie-Mellon University to support the distributed personal computing project, Spice, and the development of a fault-tolerant distributed sensor network (DSN). Accent is built around a single, powerful abstraction of communication between processes, with all kernel functions, such as device access and virtual memory management accessible through messages and distributable throughout a network. In this paper, specif ...

Keywords: Distributed computation, Inter-process communication, Network, Networking, Operating systems, PERQ, Paging, UNIX, VAX, Virtual memory

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